

J1132 U.S. PTO

**Box: PATENT APPLICATION**  
NIT-201-02

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Prior Application: N. KATOH et al  
Serial No. 09/582,327  
Filed: June 23, 2000

Group Art Unit: 2819  
Examiner: A. Tran  
For: SEMICONDUCTOR INTEGRATED CIRCUIT  
DEVICE, STORAGE MEDIUM ON WHICH CELL  
LIBRARY IS STORED AND DESIGNING METHOD  
FOR SEMICONDUCTOR INTEGRATED CIRCUIT

**REQUEST FOR DIVISIONAL APPLICATION**  
**UNDER 37 C.F.R. 1.53(b)**

Assistant Commissioner of Patents  
Washington, D.C. 20231

Sir:

This is a request for filing a divisional application under 37 C.F.R. 1.53(b) of pending prior application Serial No. 09/582,327, filed on June 23, 2000, entitled SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE, STORAGE MEDIUM ON WHICH CELL LIBRARY IS STORED AND DESIGNING METHOD FOR SEMICONDUCTOR INTEGRATED CIRCUIT, by all of the inventors named in the prior application.

1. Enclosed is a copy of the prior application, including the Declaration as originally filed.
2. The Filing Fee is calculated below:

CLAIMS AS FILED IN THE PRIOR APPLICATION  
LESS ANY CLAIMS CANCELED BY AMENDMENT BELOW  
PLUS ANY CLAIMS ADDED BY ACCOMPANYING PRELIMINARY AMENDMENT

Basic Fee							\$	740.00
Total Claims	4	-	20	=	11	x	18	= \$ 0.00
Independent Claims	2	-	3	=	0	x	84	= \$ 0.00
Total Filing Fee							\$	740.00

3. A check including the amount of \$740.00 is enclosed to cover the Filing Fee.

4. Cancel claims 1-20, 24 and 25 before calculating the Filing Fee.

5. The Commissioner is hereby authorized to charge any additional fees which may be required, or to credit any overpayment, to Deposit Account No. 50-1417.

6. Amend the specification by inserting, before the first line:

--This is a continuation application of U.S. Serial No. 09/582,327, filed June 23, 2000; which is a 371 of PCT/JP98/05688, filed December 16, 1998.--.

7. New drawings are enclosed, eighteen (18) sheets, Figs. 1-28.

8. The power of attorney is set forth in the Declaration in the prior application or an associate power of attorney is hereby granted to:

Jeffrey M. Ketchum, Registration No. 31,174  
Shrinath Malur, Registration No. 34,663  
John R. Mattingly, Registration No. 30,293  
Daniel J. Stanger, Registration No. 32,846  
Gene W. Stockman, Registration No. 21,021.

There is no change in the correspondence address.

Address all future correspondence to:

MATTINGLY, STANGER & MALUR, P.C.  
1800 Diagonal Road, Suite 370  
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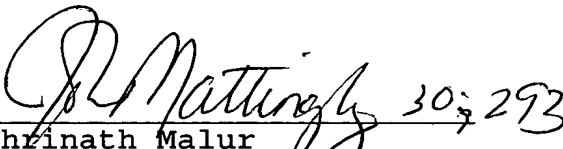
9. The prior application is assigned to Hitachi, Ltd.

10. Priority of the following Japanese patent application is claimed under 35 U.S.C. § 119:

No. 9-359277, filed December 26, 1997.

The certified priority document has been filed in the PCT application.

The undersigned hereby declares that no matter contained in the specification, including the claims, and drawings filed in the present divisional application would have been new matter in the prior application Serial No. 09/582,327, as originally filed on June 23, 2000.

  
Shrinath Malur  
Registration No. 34,663  
Attorney of Record

MATTINGLY, STANGER & MALUR  
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Date: February 28, 2002